Uniquify

2030 Fortune Drive — Suite 200 - San Jose, CA 95131

Headquartered in Silicon Valley, Uniquify (www.uniquify.com) is a privately held ASIC design services and semiconductor IP solutions provider. The company has design centers in Santa Clara, California, Pune, India, Ho Chi Minh city and Da Nang city and it maintains sales offices in Austin, Santa Clara, Seoul, Taipei and Tel Aviv. Uniquify provides leading edge SoC design and IP solutions. We offer a wide range of “ideas2silicon” services that span design specification, RTL, logic design/verification, physical implementation, and manufacturing operations. Perseus, our proprietary design management system allows us to deliver consistent design closure and reduced schedules on even the most complex SoC designs.

Job Information

Job Title: Senior Physical Design Engineer

Job Requirement

- Bachelors or Master’s degree in Engineering with 2+ years of ASIC physical design (place and route) experience.
- Proven APR hands-on tapeout ASIC experiences in 45nm and/or below technologies, fluent with Netlist-to-GDS flow using Synopsys/Cadence tools.
- Experienced to be an APR block owner or top-level integration/floorplanning/ APR engineer.
- Must be able to work independently, to collaborate with different teams, and to be flexible to take dynamic and challenging assignments. Must have good communication skills, and can take tapeout pressure.
- Experienced in Tcl/Perl scripting to innovate APR methodology.
- Low power implementation
- Familiar with synthesis (RTL to Netlist).

Applications Information

Contact Person
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